

FIG. 2
PRIOR ART

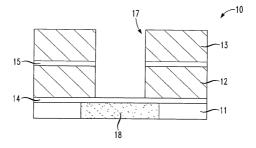


FIG. 3

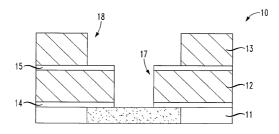


FIG. 4
PRIOR ART

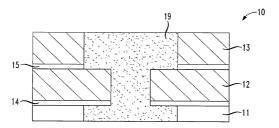


FIG. 5

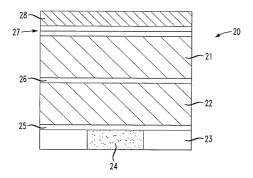


FIG. 6

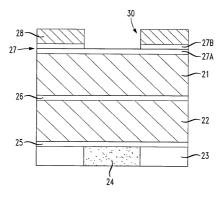


FIG. 7 PRIOR ART

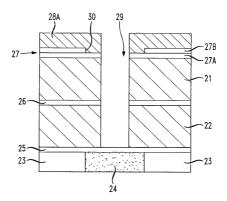


FIG. 8

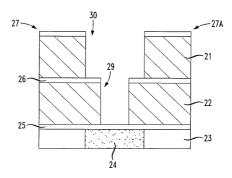


FIG. 9

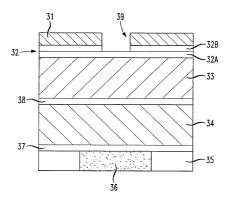


FIG. 10

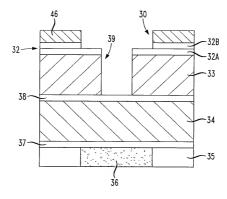


FIG. 11

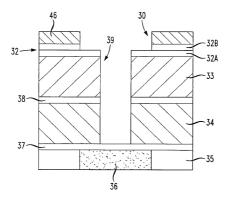
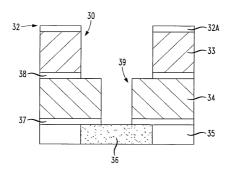


FIG. 12 PRIOR ART



HUANG 3-8-3-2-2-25-5
A Mask Layer and Interconnect Structure for Dual Damascene Fabrication of a Semiconductor Device
Robert L. Wolter (407-926-7700)
Serial #10/026,257

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FIG. 13

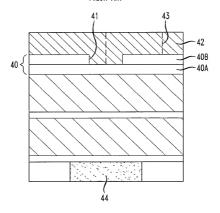
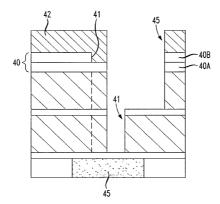


FIG. 14 PRIOR ART



HUANG 3-8-3-2-2-25-5

A Mask Layer and Interconnect Structure for Dual Damascene Fabrication of a Semiconductor Device
Robert L. Wolter (407-926-7700)
Serial #10/026,257

FIG. 15

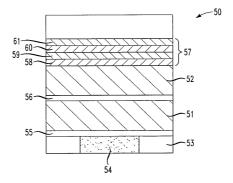
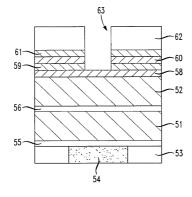


FIG. 16



HUANG 3-8-3-2-2-25-5
A Mask Layer and Interconnect Structure for Dual Damascene Fabrication of a Semiconductor Device
Robert L. Wolter (407-926-7700)
Serial #10/026,257

FIG. 17

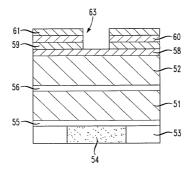


FIG. 18

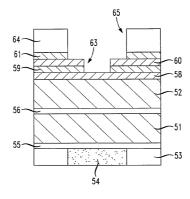


FIG. 19

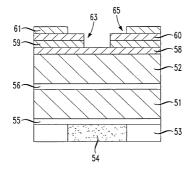


FIG. 20

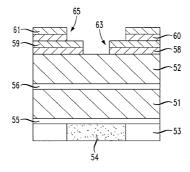


FIG. 21

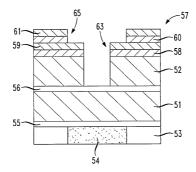


FIG. 22

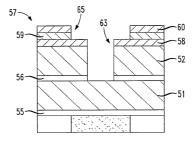


FIG. 23

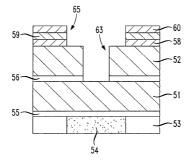


FIG. 24

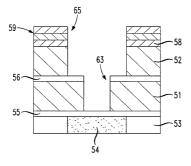


FIG. 25

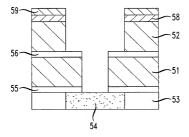


FIG. 26

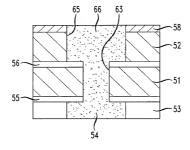


FIG. 27

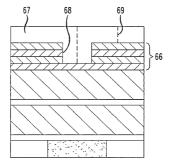
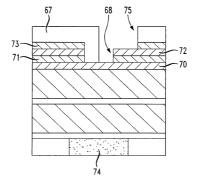


FIG. 28



 $\begin{array}{c} {\hbox{HUANG $3-8-3-2-2-25-5}}\\ \hbox{A Mask Layer and Interconnect Structure for Dual Damascene Fabrication of a Semiconductor Device} \\ \hbox{Robert L. Wolter ($407-926-7700)}\\ \hbox{Serial $\#10/026,257} \end{array}$

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FIG. 29

